Attorney Docket No.: YOR920040091US1

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re PATENT APPLICATION of:

Puneet Gupta et al.

Appln. No.:

Unknown

Art Unit:

Filed:

herewith

Examiner: Unknown

For:

INTEGRATED CIRCUIT LOGIC WITH

SELF COMPENSATING BLOCK

**DELAYS** 

## INFORMATION DISCLOSURE STATEMENT

Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir,

Pursuant to the duty of disclosure under 37 C.F.R. §1.56, submitted herewith is an Information Disclosure Statement (IDS) including a Form PTO-1449 and a copy of one (1) non-patent publication. Pursuant to a Pre-OG Notice dated July 11, 2003 waiving requirement for copies of U.S. Patents and Published Applications, copies of Patents and Published Applications cited on the PTO-1449 are not included.

Since this IDS is being submitted with the application and prior to a first office action, no certification or fee is believed required, and the requirements of 37 C.F.R. §§1.97 and 1.98 are deemed to be fully met as to all documents submitted. Consideration of the submitted documents is respectfully requested.

Respectfully Submitted,

February 26, 2004

(Date)

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Date of Deposit February 26, 2004

PTO/SB/08A (06-03)

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				First Named Inventor	Gupta et al.		
STATEMENT BY APPLICANT				Art Unit			
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Sheet	1	of	2	Attorney Docket Number	YOR920040091US1		

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Document Number  Number - Kind Code <sup>2 (# known)</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
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FOREIGN PATENT DOCUMENTS								
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		Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> ( <i>if known</i> )			Where Relevant Passages or Relevant Figures Appear	T⁵		
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Examiner	Date	
Signature	Considered	

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Translation is attached.
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INFO	<b>PRMATION DIS</b>	CLOS	URF	Filing Date		
STATEMENT BY APPLICANT				First Named Inventor	Gupta et al.	
				Art Unit		
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Sheet	2	of	2	Attorney Docket Number	YOR920040091US1	

NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>		
		ORSHANSKY ET AL., A General Proabalistic Framework for Worst Case Timing Analysis, DAC 2002, 6/10/2002, Page(s) 556-61, Published in: New Orleans, LA			
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Signature	Considered	
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